## MAY 2 1 2000 Applies an

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Lancaster

MOEMN erial No.: 09/689,442

Filed: October 12, 2000

Title: Semiconductor Non-Volatile Memory

**Device Having an Improved Write** 

Speed

Attorney Docket No.: NVX-0015C1

COP

Group Art Unit: 2822

Examiner: N/A



### CORRECTION OF FILING RECEIPT AND CORRECTION OF CORRESPONDENCE ADDRESS

Office of Initial Patent Examination Customer Service Center Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Correction of Filing Receipt.

Please correct the filing receipt. The correspondence address should be as follows:

Bradley T. Sako 3954 Loch Lomand Way Livermore, CA 94550

The following is also included:

- 1) A copy of the filing receipt showing the requested correction.
- 2) A copy of the original application transmittal for this application. The transmittal indicates the above-correspondence address.

37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit:

MAY 17, 2001

Typed Name: Bradley T. Sako

Signature:

brulge-



### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

3) A copy of a Revocation of Power of Attorney and New Power of Attorney for the Parent Application (09/082,167) of this continuation application which appoints Bradley T. Sako to transact all business connected to this application.

### Change of Correspondence Address.

Please change the correspondence address for this application to

Bradley T. Sako 3954 Loch Lomand Way Livermore, CA 94550

Respectfully Submitted,

Bradley T. Sako

Attorney

Reg. No. 37,923

Bradley T. Sako Attorney/Agent 3954 Loch Lomand Way Livermore, CA 94550 Tel. 1-408-289-5315



### United States Patent and Trademark Office

COMMISSIONER FOR PATENTS UNITED STATES PATENT AND TRADEMARK OFFICE

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ſ	APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	CIND CLAIMS	
ı	09/689,442	10/12/2000	2822	710	NVX- 0015C1	13	20	18/3 B	

Richard A Bachand Eastham Johnson Monnheimer & Jontz PC

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Bradley T. Sako 3954 Loch Lomand Way

Livermore, CA 94550

FILING RECEIPT

DEC 1 4 2000

Richard A. Bachand Attorney at LAW

Date Mailed: 12/05/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBÉR, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Loren T. Lancaster, Colorado Springs, CO;

**Continuing Data as Claimed by Applicant** 

THIS APPLICATION IS A DIV OF 09/082,167 05/20/1998 PAT 6,140,676

Foreign Applications

If Required, Foreign Filing License Granted 12/05/2000

Title

Semiconductor non-volatile memory device having an improved write speed

**Preliminary Class** 

Data entry by : DILLON, LAWANDA

Team: OIPE

Date: 12/05/2000

Page 2 of 3
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Bib Data Sheet

**CONFIRMATION NO. 7388** 

		FILING DATE 10/12/2000 RULE		CLASS 257		GROUP ART UNIT 2822		ATTORNEY DOCKET NO. NVX-0015C1		
APPLICANTS Loren T. Lancaster, Colorado Springs, CO;  ** CONTINUING DATA **********************************										
VAS I no I Met affor						INDEPENDEN CLAIMS 3				
Bradley T. Sako 3954 Loch Lomand Way Livermore ,CA 94550										
TITLE Semiconductor nor	n-vola	atile memory device ha	ving an	improved write	spee	d .				
RECEIVED No						All Fees  1.16 Fees (Filing)  1.17 Fees (Processing Ext. of time)  1.18 Fees (Issue)  Other  Credit				